



Let's shape a new
era of mobility. Together.

Let's move #LikeABosch

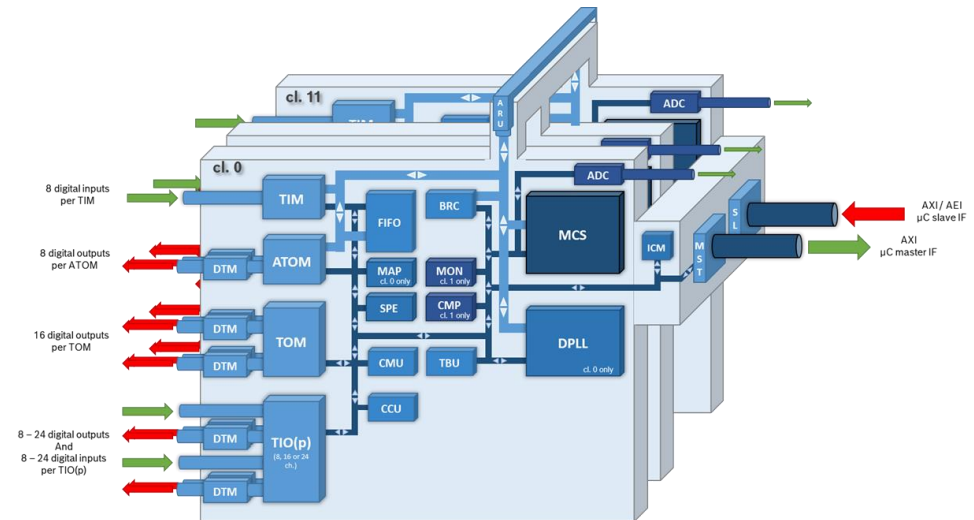
Bosch IP TechDay 2024

Advantages and Benefits of the GTM IP

ME-IC/PRM-IP | March 14th, 2024



1. GTM Introduction
2. GTM – Markets, Configs, Eco-system
3. GTM Applications
4. GTM IP



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GTM Introduction

GTM-IP

GTM – Generic Timer Module

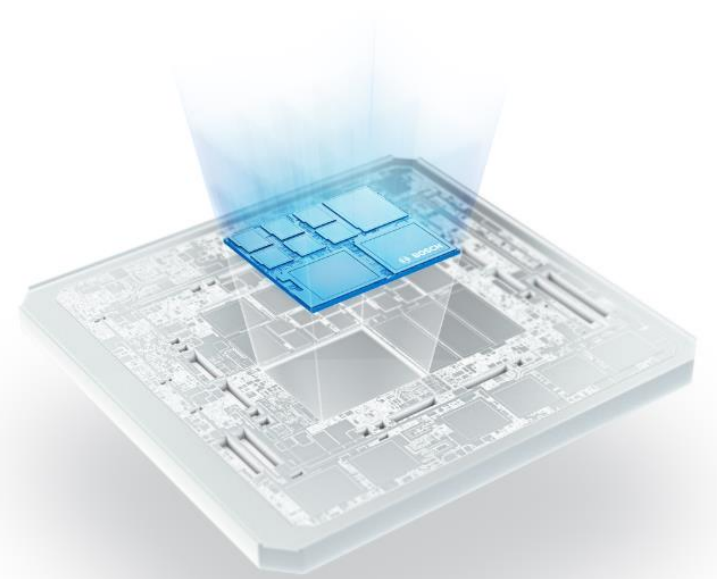
- **Timer:**

- digital input data (e.g. capturing sensor data)
- digital output data (e.g. PWMs for actor control)

- **GTM is a realtime, standardized oriented I/O co-processor**

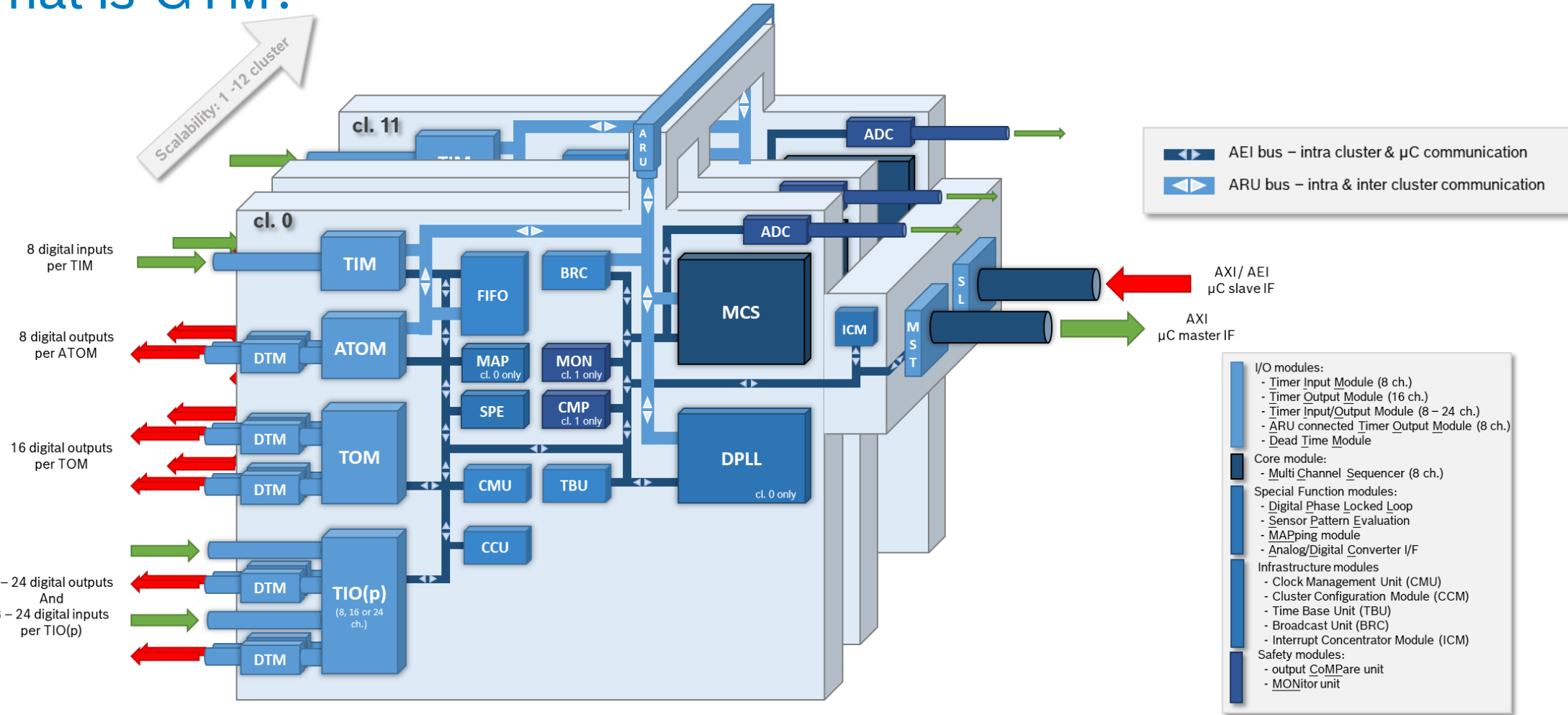
- digital input data processing (e.g. capturing sensor data and process these)
- digital output data generation (e.g. PWMs for actor control, with close-loop control)

- **Offloading real-time critical I/O workloads from μ C cores to a specialized I/O co-processor with massive parallel thread handling capabilities**

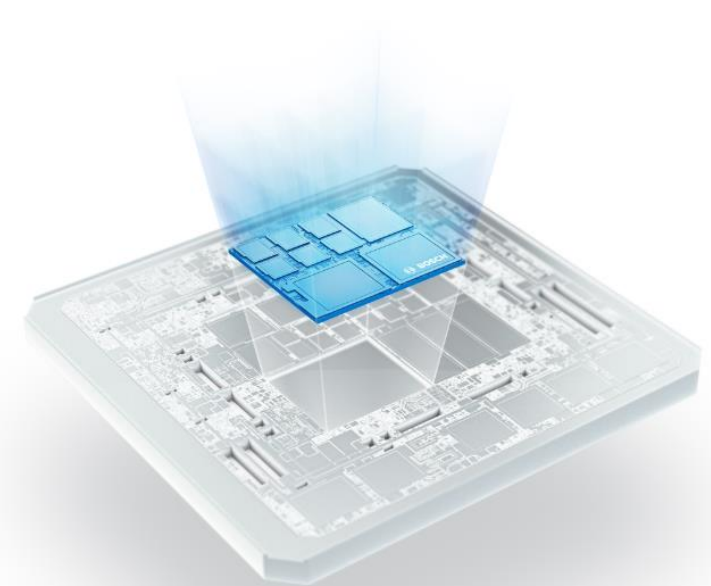


GTM-IP – Architecture

What is GTM?

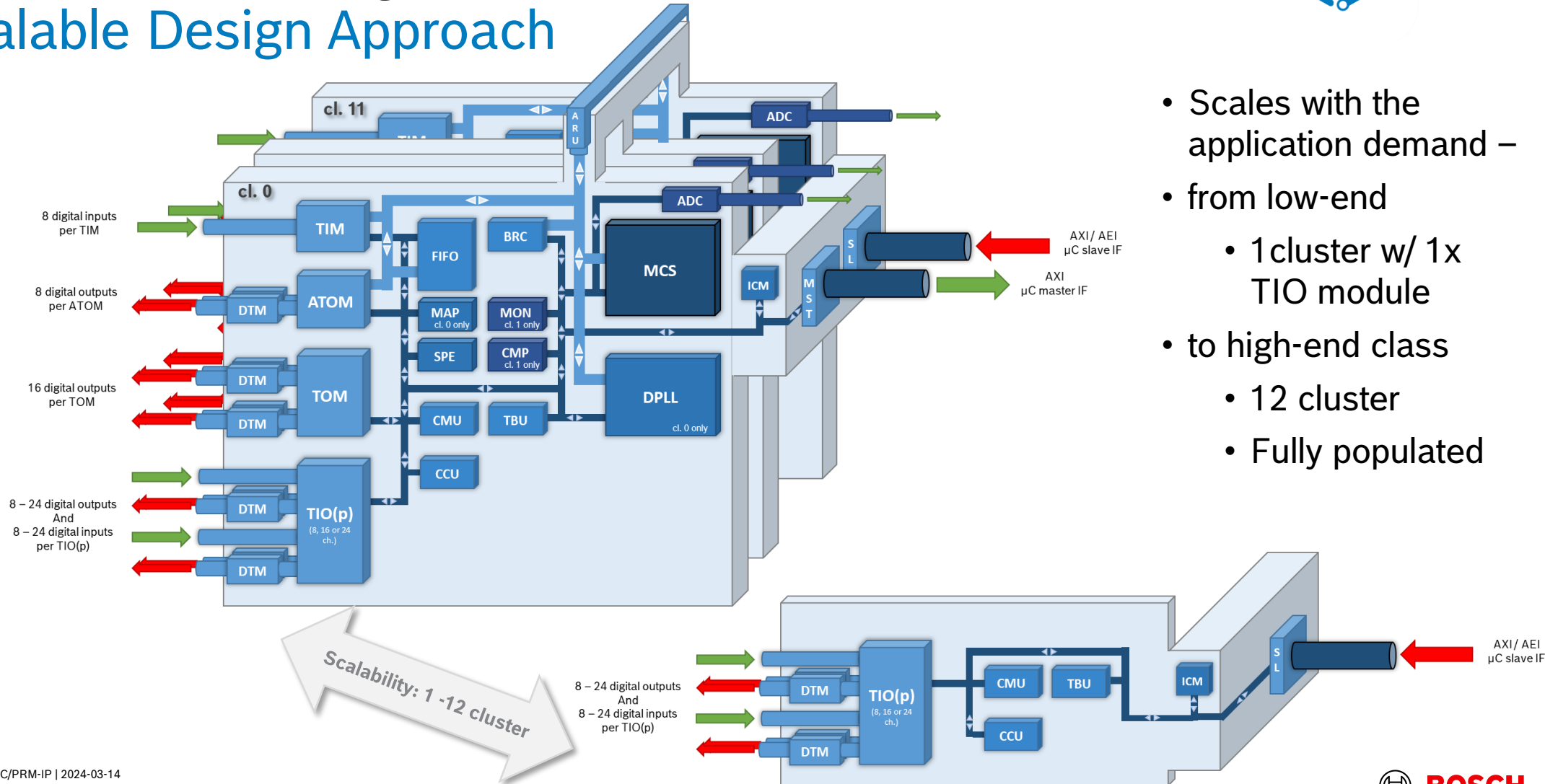


- **Common architecture across multiple semiconductors**
 - Development of software/applications independent of selected MCU
- **Generic architecture**
 - Covering a wide range of application domains and areas (powertrain, traction control, chassis control, xEV, inverter, converter PFC, industry, ...)
- **Open to 3rd parties to contribute to nextGen requirements**
- **Growing Eco-Environment**
 - Enables development of a rich set of tools and compilers



GTM-IP – Advantage & Benefits

Scalable Design Approach



- Scales with the application demand –
- from low-end
 - 1 cluster w/ 1x TIO module
- to high-end class
 - 12 cluster
 - Fully populated

GTM-IP – Architecture

Key features and structures

Real-time oriented I/O co-processor

1. Modular design approach

- tailorable to the target application

2. Rich set of multi-purpose and specialized modules

3. RISC-based internal cores with 8x multi-threading

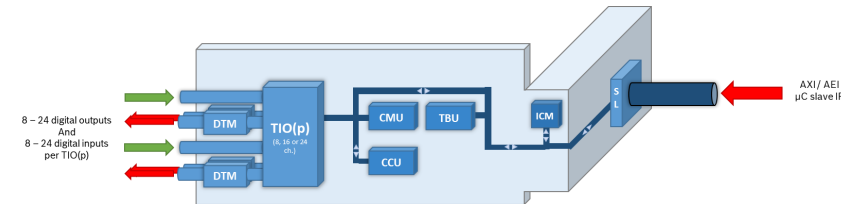
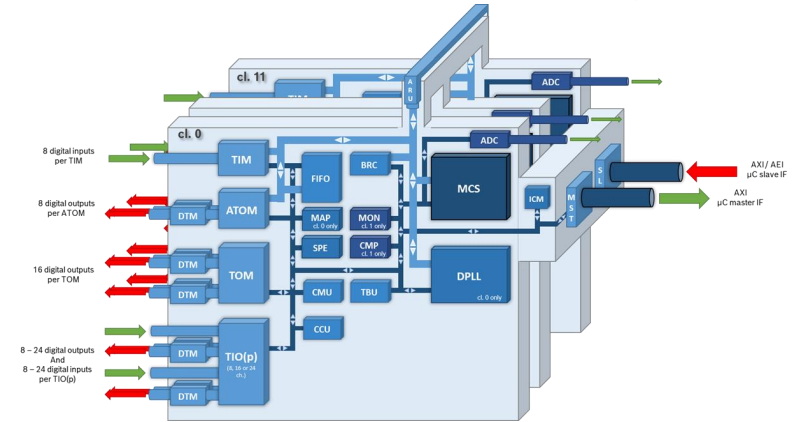
4. Two levels of GTM internal bus systems to connect sub-modules

- intra- and inter-cluster communication

5. μ C bus master interface

- enables offloading of workloads from μ C cores to GTM

6. Programmability by external CPUs

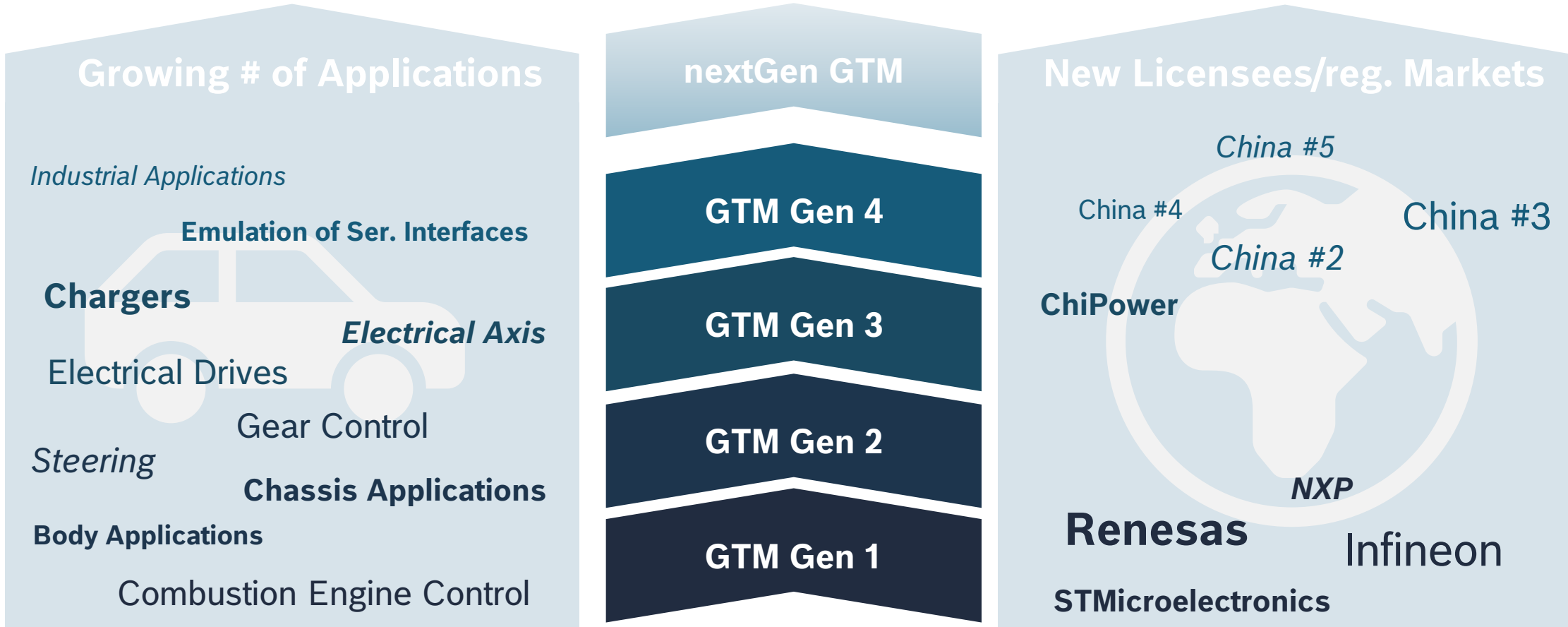


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GTM – Markets, Configs, Eco-system

GTM-IP – Status and Roadmap

New markets – application driven growth



GTM-IP – Overview GTM Gen 1 – Gen 4

Delivered device configuration 2010 - 2023



GTM IP Deliveries

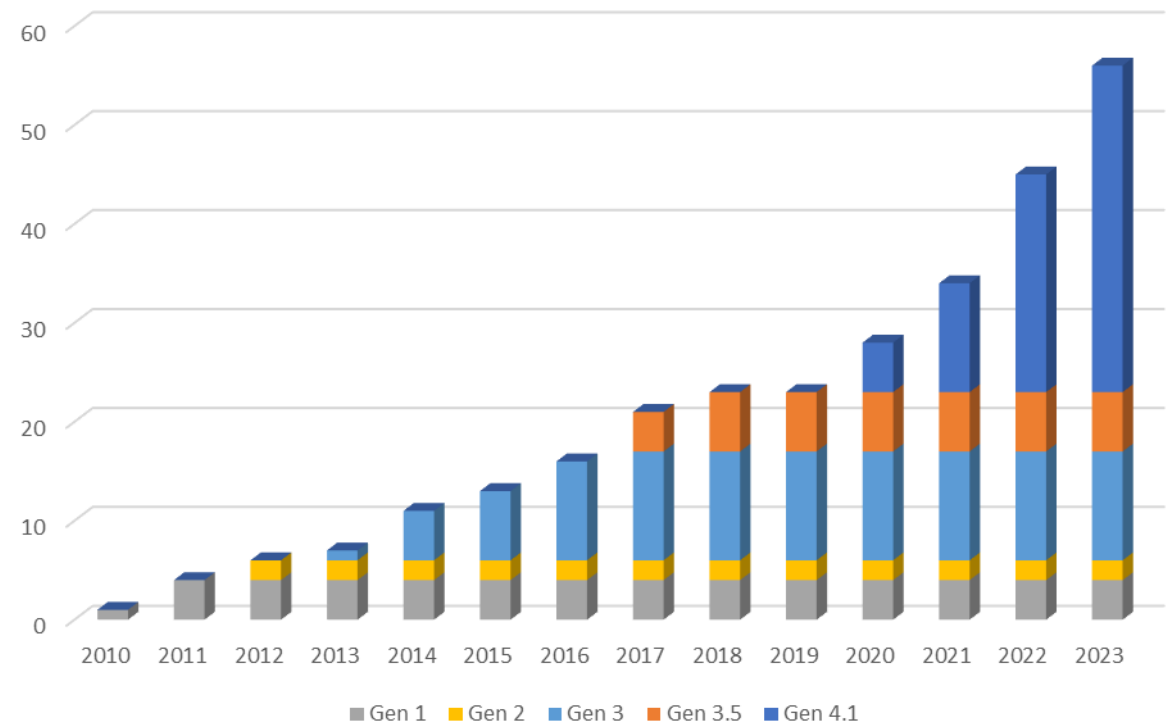
First GTM IP (Gen 1) delivered in 2010

Meanwhile more than 50 different configurations delivered to multiple semiconductor vendors

Delivery of GTM IP Gen 4 starting in 2020/21

GTM Gen 4.1 devices dominating the list with more than Gen 3/3.5 in less than 2 years

Delivered GTM configurations
(by GTM IP Generation)



GTM-IP

GTM IP 4.1 microcontrollers

Microcontrollers from semiconductor companies

- **Chippower THA6** (GTM 3)



- **Infineon Aurix TC4x family**

- **NXP S32Z2 / S32E2**

- **Renesas RH850/U2B**



- **ST Microelectronics Stellar G/P family**



MCS Assembler

Tools and services from Bosch and partners

Continuously growing Eco-Environment 

- Rich set of tools and compilers supporting application development including assembler and C compilers
- GTM System C reference model support and integration in various MCU prototypes
- Virtual Prototyping
- Software drivers and application-based library support
- Debugger support for efficient development and analysis of GTM applications
- GTM training and application notes



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GTM Applications

GTM Applications

Example Application

- Electric Powertrain
 - Inverter, motor control, sensor, ...
- Gasoline Powertrain
 - injection control, fuel pump, sensor,...
- Converter
 - DC/DC, AC/DC, PFC,
- Inverter
 - DC/AC,
- Breaks
- Steering
- Motor Control,
 - BLDC (Brush Less DC)
 - FOC (Field Oriented Control), ...
- Emulation of serial I/F
 - SPI, UART ,LIN ,I2C, I3C
- Control of Display
- Replacing external complex logic to reduce BOM cost
- and many other application

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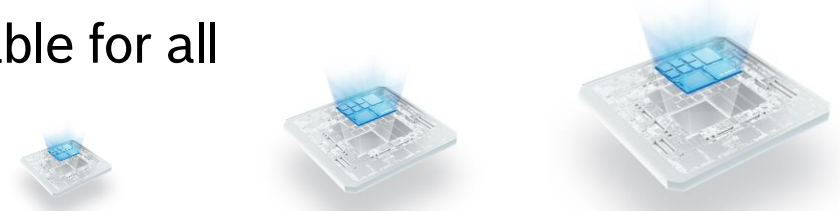
GTM 4.1 IP

GTM 4.1 IP

Standard & Customer tailored GTM IP configurations

- GTM Standard Configs
 - 8 standard configurations available
 - Well defined standard configuration which are suitable for all kind of application
 - Available off the shelf

- GTM Customer defined
 - Tailored GTM configuration for customers need
 - Excel-based order sheet to allow customers to tailor the GTM IP configuration towards the customers needs



Item No.	Description	Unit	Channel 1	Channel 2	Channel 3	Channel 4	Channel 5	Channel 6	Channel 7	Channel 8	Channel 9	Channel 10
001	Channel 1 Base/Back to 001	1										
002	Channel 2 Base/Back to 002	1										
003	Channel 3 Base/Back to 003	1										
004	Channel 4 Base/Back to 004	1										
005	Channel 5 Base/Back to 005	1										
006	Channel 6 Base/Back to 006	1										
007	Channel 7 Base/Back to 007	1										
008	Channel 8 Base/Back to 008	1										
009	Channel 9 Base/Back to 009	1										
010	Channel 10 Base/Back to 010	1										

GTM-IP

GTM IP 4.1

Revision 0130

Release November 2022



Deliverables per each GTM 4.1 device configuration

- ▶ Documentation
 - Readme file
 - GTM Spec 4.1 and Appendix
 - Module Integration Guide
- ▶ Design
 - DFT results
 - Spyglass reports
 - Synthesis constraints
- ▶ Safety
 - Safety Manual & Safety Certificate
 - FMEDA and DFA



Thank you!

Gregor Sunderdick, Product Manager